

07/19/00

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

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SCANNED

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PATENT DATE

APPLICATION NO. 09/618971	CONT/PRIOR	CLASS 325 703	SUBCLASS 28	ART UNIT 2819 2123	EXAMINER THANGAVELU
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TITLE	APPLICANTS
1. 1990-1991	
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Vikram Gupta

Method for designing mixed signal integrated circuits and configurable synchronous digital noise emulator circuit

PTO-2040
12/99.

ISSUING CLASSIFICATION

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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